

Amendments to the Specification

Please replace the paragraph beginning on page 5, line 9, with the following:

In Figure 2, the data comes into the correlator 24 of Figure 1. This data is stored in a register 34 14, typically an oversampling shift register. Each bit of this data is then compared individually against each bit of the reference code. Typically, the reference code is stored in a set of data latches, which may be flip-flops or any other type of storage, such as 36a-36n. As used here, the variable ~~N~~ or n indicates the size of the code. This corresponds to the number of data latches and the size of the register used.